## METHOD FOR TRANSLATING TWO-DIMENSIONAL CELL-COORDINATES OF A MEMORY PRODUCT TO N-DIMENSIONAL PHYSICAL ADDRESSES

## **ABSTRACT**

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A structure and method for translating address buffer coordinates for a device under test having two or more similar repeatable units. The method comprises identifying a repeatable unit of the repeatable units, preparing a look up table for translating buffer coordinates of a reference unit of the repeatable units and displacing information from the look up table to correspond to the repeatable units.